

# Download Motorola 6809 Site Controller Manual

The Motorola 6809 ("sixty-eight-oh-nine") is an 8-bit microprocessor CPU with some 16-bit features from Motorola. It was designed by Terry Ritter and Joel Boney and introduced in 1978. It was a major advance over both its predecessor, the Motorola 6800, and the related MOS Technology 6502. Among the systems to use the 6809 are the Dragon home computers, TRS-80 Color Computer, the Vectrex home ... The HP 64000 Logic Development System, introduced 17 September 1979, is a tool for developing hardware and software for products based on commercial microprocessors from a variety of manufacturers. The systems assisted software development with assemblers and compilers for Pascal and C, provided hardware for in-circuit emulation of processors and memory, had debugging tools including logic ...

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Introduction and Interfacing of the Motorola® Quantar® station Originally by Author Unknown Reworked with new images by Robert W. Meister WA1MIK April 6th 2012: PCBs. New PCB's arrived, vacuum sealed for freshness ! New batch of factory fresh PCB's These are for the video "sync-cleaner" boards, that take a standard def. composite video sync and 'clean it up' to output a 5v CMOS level composite sync with a more accurate NTSC spec horizontal sync duration.